Electronic Acknowledgement Receipt

EFS ID:	1228883
Application Number:	10803792
Confirmation Number:	5596
Title of Invention:	Semiconductor integrated circuit having a number of data output pins capable of selectively providing output signals and test method thereof
First Named Inventor:	Nam-Jung Her
Customer Number:	20575
Filer:	Hosoon Lee/Stormi Davis
Filer Authorized By:	Hosoon Lee
Attorney Docket Number:	4591-363
Receipt Date:	29-SEP-2006
Filing Date:	17-MAR-2004
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Application Type:	Utility
International Application Number:	

Payment information:

Submitted with Payment	no

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		4591363a.pdf	555684	yes	9

	Multipart Description		
	Doc Desc	**Start**	**End**
	Miscellaneous Incoming Letter	1	1
	Amendment - After Non-Final Rejection	2	9

Warnings:	
Information:	
Total Files Size (in bytes):	555684

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New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.